Hits	L# Hits Search Text		Search Text		DBS	Time Stamp	Comments	Error Definition	Erro
BRS L16 8837 clock same driver and (integrated a	8837 clock same driver and (integrated	clock same driver and (integrated	clock same driver and (integrated a	adj circuit)	US-PGPUB; DERWENT	OSFA1, US-PGPUB; 2003/02/10 11:13 DERWENT			0
BRS L20 1952 clock same driver and (integrated a	clock same driver and (integrated interconnect	clock same driver and (integrated interconnect	iriver and (integrated	adj circuit) and USPAT; US-PGPU DERWEN	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; 2003/02/10 11:13 DERWENT			0
BRS L28 10 (clock same grid) same driver and (integrated adj US-PGPU Circuit) and interconnect and skew DERWEN	01		(clock same grid) same driver and (circuit) and interconnect and skew	integrated adj	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; 2003/02/10 11:16 DERWENT			0
er and d skev	(clock same grid) same driver and circuit) and interconnect and skew transmission	(clock same grid) same driver and (in circuit) and interconnect and skew an transmission	(clock same grid) same driver and (in circuit) and interconnect and skew an transmission	tegrated adj d	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; 2003/02/10 11:14 DERWENT			
BRS L24 556 clock same driver and (integrated ad interconnect and skew	clock same driver and (integrated interconnect and skew	clock same driver and (integrated interconnect and skew	(integrated	adj circuit) and USPAT; US-PGPU DERWEN	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; 2003/02/10 11:22 DERWENT			0
10 BRS L36 97 circuit) and interconnect and skew	67		clock same driver and grid and (integ circuit) and interconnect and skew	rated adj	USPAT; US-PGPUB; DERWENT	USPAT; US-PGPUB; 2003/02/10 11:23 DER WENT			0

